

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application of:	)	
Shunpei YAMAZAKI et al	)	"Express Mail" Mailing Label No. <u>EV 321709204</u>
Serial No.: Not Assigned	)	Date of Deposit <u>July 29, 2003</u>
Filed: Herewith	)	
Art Unit:	)	I hereby certify that this correspondence is being
	)	deposited with the United States Postal Service
	)	"Express Mail Post Office to Addressee" service
	)	under 37 CFR1.10 on the date indicated above
	)	and is addressed to: Commissioner for Patents,
	)	P.O. Box 1450, Alexandria, VA 22313-1450
For: WIRING MATERIAL AND A	)	Name: <u>Yue X Luan</u>
SEMICONDUCTOR DEVICE HAVING WIRING	)	(typed or printed)
USING THE MATERIAL, AND THE	)	
MANUFACTURING METHOD THEREOF	)	Signature <u>Yue X Luan</u>

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. A copy of each of the references was submitted to the Patent Office or was cited by the Examiner in the parent application, Serial No. 09/527,437, filed March 16, 2000, which is being relied upon for an earlier filing date under 35 U.S.C. 120 (37 C.F.R. §1.98(d)).

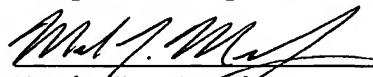
No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

A first office action, notice of allowance or issue fee notification has not been received in this case, so applicant does not believe that a fee is due. However, if any such fee is required, please charge our Deposit Account No. 50/1039.

Respectfully submitted,

  
Mark J. Murphy  
Registration No: 34,225

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LIST OF PUBLICATIONS CITED BY APPLICANT			<u>Atty. Docket No.</u> 0553-0166.01		<u>Serial No.</u> Not Assigned	
			<u>Applicant</u> Shunpei YAMAZAKI et al			
			<u>Filing Date</u> Herewith		<u>Group</u>	
U.S. PATENT DOCUMENTS						
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	5,594,569	01/14/97	Konuma et al	349	122	07/20/94
	5,643,826	07/01/97	Ohtani et al	437	88	10/25/94
	5,738,948	04/14/98	Ikedo et al	428	663	09/29/95
	5,913,100	06/15/99	Kohsaka et al	428	546	09/06/96
	5,923,962	07/13/99	Ohtani et al	438	150	04/28/95
	6,088,070	07/11/00	Ohtani et al	349	38	01/16/98
	6,303,483	10/16/01	Kunikiyo	438	592	01/08/01
	6,313,481	11/06/01	Ohtani et al	257	59	07/19/99
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	NAME	English Abstract	English Trans.	FILING DATE
	JP 5-093267	04/16/93	Hitachi Metals Ltd.	X		10/01/91
	JP 5-263226	10/12/93	Fujitsu Ltd.	X		03/17/92
	JP 7-076771	03/20/95	Japan Energy Corp.	X		09/08/93
	JP 7-130652	05/19/95	Semiconductor Energy Lab Co. Ltd.	X		10/29/93
	JP 8-153722	06/11/96	Toshiba Corp.	X		09/29/95
	WO 95/16797	06/22/96	Toshiba KK	X		12/14/94
	EP 735152 A1	10/02/96	Kabushiki Kaisha Toshiba	X		12/14/94
	JP 11-133463	08/07/98	Semiconductor Energy Lab Co. Ltd.	X		01/17/97
	JP 10-247735	09/14/98	Semiconductor Energy Lab Co. Ltd.	X		03/03/97
	JP 11-133463	05/21/97	Semiconductor Energy Lab Co. Ltd.	X		10/31/97
	EP 947593 A2	10/06/99	Kabushiki Kaisha Toshiba	X		12/14/94
	JP 2001- 056485	02/27/01	Semiconductor Energy Lab Co. Ltd.	X		07/22/99

OTHER PRIOR ART - NON-PATENT LITERATURE DOCUMENTS  
(Including Author, Title, Date, Pertinent Pages)

- 1) YOSHIDA, T. et al, "A Full Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 Digest, pp. 841-844, (1997).
- 2) FURUE, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCDC Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," SID 98 Digest, pp. 782-785, (1998).

EXAMINER:

DATE CONSIDERED:

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.